Luminary Controller
Serial Communications

Application Note

Industrial Indexing Systems, Inc.
REVISION HISTORY:

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   1) Initial release. lac
Luminary Series Controller (LSC) Serial Commands

This document defines the communications protocol of the Luminary Series Controller.

The Luminary Series Controller can be interfaced to a host computer system using serial communications. The protocol is designed to handle point-to-point and multidrop communications hardware. The host can control the operation of the LSC in the following ways:

1. Read and write integer (2 or 4 byte) and floating point (8 byte) variables.
2. Read the Luminary Controller 32-bit "Status Word".
3. Read user flags and I/O.
4. Set user flags and I/O.
5. Trace program execution and trap the value of up to two variables at each line traced.
6. Download an executable program with associated configuration data.
7. Reset the program and variable areas of the Luminary Controller.
8. Stop program execution.
9. Start program execution.

Serial commands are implemented using a Packet concept. A Packet consists of:

1. The Packet Header (12 bytes). This area is defined by a packet prefix (3 bytes), the controller type (1 byte), the controller ID (1 byte), a reserved area (2 bytes), the security level (1 byte), the packet body length (2 bytes) and operation code (2 bytes).
2. The Packet Body ('N' bytes). This variable length area contains ASCII characters and binary data representing addresses, values and other parameters.
3. The Packet Checksum (1 byte). Each byte of the Packet beginning with the controller type byte to and including the last byte of the Packet Body are summed. The Packet Checksum is equal to '0' minus the least significant byte of this value.

The host must initiate all serial communications as if the LSC is a passive device with respect to Packet Protocol.

The Packet Header, Packet Body and Packet Checksum are generated using the protocol described in the remainder of this document.
Data Transfer Protocol

This protocol allows data to be sent between the host computer and the LSC.

In data communications, the protocol defines the rules for the electrical, physical and functional characteristics of the communication link. The protocol contains procedures required to ensure an orderly exchange of information through the link, to and from the executing programs.

The LSC is initially a “passive” device with respect to Packet transmission. It will wait for Packet requests from other devices and then respond to those requests where appropriate. It is the responsibility of the host to initiate communications with the LSC.

When the LSC receives a Packet, it responds to the host with either an Acknowledgement Character (ACK 0x06) or a Negative Acknowledgement Code (NAK Code). An ACK Character is an indication that the Packet received was valid. A NAK Code is an indication that the Packet received was invalid or a timeout occurred.

It is the responsibility of the host device to retransmit a Packet in the event that it receives a NAK Code from the LSC.

<table>
<thead>
<tr>
<th>Communications Error</th>
<th>NAK Code (hex)</th>
<th>Code Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Address Out of Range</td>
<td>0x10</td>
<td>Tried to access a memory address out-of-range.</td>
</tr>
<tr>
<td>Receive Data Length</td>
<td>0x11</td>
<td>Tried to send a packet that’s too large.</td>
</tr>
<tr>
<td>Bad Data / Flag Type</td>
<td>0x12</td>
<td>Tried to read/write an invalid type.</td>
</tr>
<tr>
<td>Request Exceeded Maximum Packet Length</td>
<td>0x13</td>
<td>Tried to request too much data.</td>
</tr>
<tr>
<td>Data is Read-Only</td>
<td>0x14</td>
<td>Tried to change a read-only data type.</td>
</tr>
<tr>
<td>Bad Checksum</td>
<td>0x15</td>
<td>Error in checksum.</td>
</tr>
<tr>
<td>Timeout</td>
<td>0x16</td>
<td>Packet Transmission Stopped Midstream</td>
</tr>
<tr>
<td>Bad Header</td>
<td>0x17</td>
<td>Incorrect Packet Header Format</td>
</tr>
<tr>
<td>Bad Opcode</td>
<td>0x18</td>
<td>Tried to execute an opcode that is not supported.</td>
</tr>
<tr>
<td>Program Load Error</td>
<td>0x1f</td>
<td>Program loaded out of sequence.</td>
</tr>
</tbody>
</table>
Data Transfer Sequence

The following describes a typical sequence of events for the transfer of Packets between devices:

1. The host device creates and sends a Packet to the LSC.

2. The LSC receives the Packet. The Packet is examined for a valid Packet Header, Packet Body and Packet Checksum.

3. The LSC will respond with an ACK Character if the Packet Header, Packet Body and Packet Checksum are correct.

   The LSC will respond with a NAK Code if a problem was found with the Packet. For example, an incorrect Packet Checksum was received or a packet data length that exceeds the input buffer size.

4. If the Packet from the host is requesting information, the LSC will then retrieve the information, build a return Packet and transmit this Packet back to the host device.

   NOTE
   The LSC will timeout and reset the incoming packet protocol state machine in the event the host transmission of a packet stops for more than 2 seconds.
LSC Memory Partitions

LSC memory is partitioned in the following manner:

**Program Memory**
This is the area containing the users application program. The physical memory type of this area is Flash ROM, which is booted into SDRAM at startup.

The *Program Memory* size is 64 K bytes.

**Configuration Memory**
This is the area containing the users application configuration. The physical memory type of this area is Flash ROM, which is loaded into SDRAM at startup.

The *Configuration Memory* size is 16 K bytes.

**Data Variable Memory – Text / SHORT Integer / LONG Integer / FLOATING Point**
This is the area containing the variables used by the application program. All Integer and Floating Point variables are stored here. The physical memory type of this area is NOVRAM.

The *Data Variable Memory* size is 16 K bytes.

**Data Constants Memory - SHORT Integer / LONG Integer / FLOATING Point**
This is the area containing the constants used by the application program. All Integer and Floating Point constants are stored here. The physical memory type of this area is Flash ROM, which is booted into SDRAM at startup.

The *Data Constants Memory* size is 16 K bytes.

**Extended Data Memory - SHORT Integer / LONG Integer**
This is the area containing the cam values generated as a result of one or more `calc_cam` commands. The physical memory type of this area is SDRAM.

The *Extended Data Memory* size is 640 K bytes.

**Fixed Variable Memory – SHORT Integer / LONG Integer / FLOATING Point**
This is another area containing variables used by the application program. This area of memory is not affected by a program RESET or DOWNLOAD. Only Long Variables, Short Variables and Floating Point variables are stored here. This area cannot hold the data defined by the `begin_data / data / end_data` construct. The physical memory type of this area is NOVRAM.

The *Fixed Variable Memory* size is 8 K bytes.

**Data Memory Addressing**

Each *Data Memory* area can be addressed directly using a 32-bit absolute pointer.
Packet Data Structure

<table>
<thead>
<tr>
<th>Packet Header</th>
<th>Packet Body</th>
<th>Packet Checksum</th>
</tr>
</thead>
<tbody>
<tr>
<td>(12 bytes)</td>
<td>(Variable Length - 496 bytes maximum)</td>
<td>(1 byte)</td>
</tr>
</tbody>
</table>

The Packet Data Structure is comprised of three main sections; the **Header**, the **Packet Body** and the **Checksum**. The maximum size of a packet must not exceed 509 bytes.

**Packet Header**

<table>
<thead>
<tr>
<th>“ESC”</th>
<th>Type</th>
<th>ID</th>
<th>Reserved</th>
<th>Level</th>
<th>Packet Body Length</th>
<th>Operation Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>(3 bytes)</td>
<td>(1 byte)</td>
<td>(1 byte)</td>
<td>(2 bytes)</td>
<td>(1 byte)</td>
<td>(2 bytes)</td>
<td>(2 bytes)</td>
</tr>
</tbody>
</table>

The first 3 bytes are the **packet prefix** character string. It will always be the 3-byte character string “ESC” and is used to signal the beginning of a **Packet**.

The next byte is the **controller type**. This is a fixed value of [0x08] for the LSC controller.

The next byte is the **controller id**. This is used to identify a controller on the multi-drop RS485 interface. This value must be in the range of 1 to 255. (The ID is ignored on RS232 Transmissions).

The next 2 bytes are **reserved** for future use.

The next byte is the **level** setting. The value of this byte will be in the range of 0 to 255. This byte will be used to identify the sender of a packet. This gives IIS the flexibility at the Luminary Controller to accept or refuse a packet command.

The next 2 bytes define the **packet body length** (the **Packet Header** and the **Packet Checksum** are not included). The length is specified in bytes and may be an even or odd value. The most significant byte of the length is transmitted first.

The next 2 bytes define the **operation code**. Valid **operation codes** are listed below.

**Packet Body**

The variable length **Packet Body** is next. The maximum length of the operation data is 496 bytes.
Packet Checksum

<table>
<thead>
<tr>
<th>Checksum Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>(1 byte)</td>
</tr>
</tbody>
</table>

Following the Packet Body, there is a one-byte field, which contains the Packet Checksum for the Packet. Each byte of the Packet beginning with the controller type byte to and including the last byte of the Packet Body are summed. The Packet Checksum is equal to ‘0’ minus the least significant byte of this value.
### Operation Codes

<table>
<thead>
<tr>
<th>Code</th>
<th>Operation</th>
<th>Passed</th>
<th>Returned</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>Read Block Data</td>
<td>Data Type, Starting Address, # of Values to Read</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDBLKDATA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>02</td>
<td>Write Block Data</td>
<td>Data Type, Starting Address, Values to Write</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(WRBLKDATA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>03</td>
<td>Request LSC Status</td>
<td>None</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RQSTAT)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>04</td>
<td>Set Flag</td>
<td>Flag Type, Logical Flag #</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(SETFLAG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>05</td>
<td>Clear Flag</td>
<td>Flag Type, Logical Flag #</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(CLRFLAG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>06</td>
<td>Read Flag</td>
<td>Flag Type, Logical Flag #</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDFLAG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>07</td>
<td>Read Flag Group</td>
<td>Flag Type</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDFLAGGROUP)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>15</td>
<td>Stop Program</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(STOPPRG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>16</td>
<td>Reset Program</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(RESETPRG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>17</td>
<td>Start Program</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(STARTPRG)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>18</td>
<td>Enable AutoStart</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(SETAUTOSTART)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>19</td>
<td>Disable AutoStart</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(CLRAUTOSTART)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>Read Program Information</td>
<td></td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDPREGINFO)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>21</td>
<td>Read Controller Info.</td>
<td></td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDCNTRLINFO)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>26</td>
<td>Read Watch Data</td>
<td></td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDWATCHDATA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>27</td>
<td>Set Flag Addressable</td>
<td>Device Number, Flag Type, Physical Flag Number</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(SET_FLAGA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>28</td>
<td>Clear Flag Addressable</td>
<td>Device Number, Flag Type, Physical Flag Number</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(CLR_FLAGA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>29</td>
<td>Read Flag Addressable</td>
<td>Device Number, Flag Type, Physical Flag Number</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td></td>
<td>(RD_FLAGA)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>Read Flag Group Addressable</td>
<td>Device Number, Flag Type, Number of Bytes</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RD_FLAGA_GROUP)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>31</td>
<td>Read Event Information</td>
<td>None</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td></td>
<td>(RDEVENTINFO)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**Operation Codes (continued)**

<table>
<thead>
<tr>
<th>Code</th>
<th>Operation</th>
<th>Passed</th>
<th>Returned</th>
</tr>
</thead>
<tbody>
<tr>
<td>35</td>
<td>Read Error Log (RDERRORLOG)</td>
<td>None</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td>36</td>
<td>Clear Fixed Memory (CLRFIXEDMEM)</td>
<td>None</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>64</td>
<td>Write First Program Packet (WRPRG)</td>
<td>Address, #Bytes, Data</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>65</td>
<td>Write Additional Program Packets (WRMOREPRG)</td>
<td>Address, #Bytes, Data</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>66</td>
<td>Write Program Information (WRPRGINFO)</td>
<td></td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>68</td>
<td>Request Trace Status (RQTRCSTAT)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td>69</td>
<td>Start Trace (STARTTRC)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>70</td>
<td>Stop Trace (STOPTRC)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>71</td>
<td>Write Trace Setup (WRTRCSETUP)</td>
<td>Mode, Trace Enable, Trace Trigger, Lines to Trace, Var 1 Addr, Var 2 Addr, Var 1 Type, Var 2 Type</td>
<td>NAK code or ACK char</td>
</tr>
<tr>
<td>72</td>
<td>Read Trace Lines (RDTRCLINES)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td>73</td>
<td>Read Trace Variable 1 (RDTRCVAR1)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
<tr>
<td>74</td>
<td>Read Trace Variable 2 (RDTRCVAR2)</td>
<td>(Reserved Value)</td>
<td>NAK code or ACK char followed by Data</td>
</tr>
</tbody>
</table>
## Controller Data / Flag Types

<table>
<thead>
<tr>
<th>Type</th>
<th>Description</th>
<th>R/W</th>
<th>Device Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Luminary Controller Status</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>2.</td>
<td>Luminary Device Status</td>
<td>read only</td>
<td>yes</td>
</tr>
<tr>
<td>3.</td>
<td>Luminary Device Input</td>
<td>read only</td>
<td>yes</td>
</tr>
<tr>
<td>4.</td>
<td>Luminary Device Output</td>
<td>read / write</td>
<td>yes</td>
</tr>
<tr>
<td>5.</td>
<td>Timer</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>6.</td>
<td>User Flag</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>7.</td>
<td>Global S/W PLS</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>8.</td>
<td>Global H/W PLS</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>9.</td>
<td>Not used</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10.</td>
<td>Luminary Controller Input</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>11.</td>
<td>Luminary Controller Output</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>12.</td>
<td>Luminary Controller PLS</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>13.</td>
<td>Remote Controller Input</td>
<td>read only</td>
<td>no</td>
</tr>
<tr>
<td>14.</td>
<td>Remote Controller Output</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>15.</td>
<td>Remote Device Status</td>
<td>read only</td>
<td>yes</td>
</tr>
<tr>
<td>16.</td>
<td>Remote Device Input</td>
<td>read only</td>
<td>yes</td>
</tr>
<tr>
<td>17.</td>
<td>Remote Device Output</td>
<td>read / write</td>
<td>yes</td>
</tr>
<tr>
<td>18 - 49.</td>
<td>Not used</td>
<td></td>
<td></td>
</tr>
<tr>
<td>50.</td>
<td>Data – SHORT</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>51.</td>
<td>Data – LONG</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>52.</td>
<td>Data – FLOAT</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>53.</td>
<td>Data – TEXT</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>54.</td>
<td>Data – SHORT Extended Memory</td>
<td>read / write</td>
<td>no</td>
</tr>
<tr>
<td>55.</td>
<td>Data – LONG Extended Memory</td>
<td>read / write</td>
<td>no</td>
</tr>
</tbody>
</table>
General Serial Command Format

The LSC will accept two basic types of serial commands:

1 - The first type of command is a transfer of data from a host device to the LSC. Once the LSC receives this type of Packet, it simply responds to the host with an ACK character or NAK code. Whatever data or command was received is processed by the LSC.

2 - The second type of command is a request for data from a host device to the LSC. Once the LSC receives this type of Packet, it responds to the host with an ACK character or NAK code.

   After responding with an ACK character, the LSC will process the request, construct a Packet of information to be sent back to the host device, and then transmit that Packet.

Each serial command consists of an Operation Code, optionally followed by one or more parameters. The complete command must be formatted into a Packet by defining the Packet Header, followed by the Packet Body and the Packet Checksum.

If the command is one that requires a response from the LSC, the LSC will respond with a similar Packet Header along with the requested data.

In the format description for each Packet, [Header] represents the Packet Header, and [Checksum] represents the Packet Checksum. The brackets themselves are for purposes of clarity and are NOT part of the actual Packet.

All numbers in binary format are most significant byte first, followed by least significant byte(s).
The RDBLKDATA Command (Operation Code 01)

The RDBLKDATA serial command reads a group of data bytes from the LSC. Each data value is stored sequentially in the LSC Data Area beginning at the address specified in the RDBLKDATA command.

Packet sent to the LSC:

[Header][Address][#Bytes][Checksum]

Address: A 4-byte value indicating the absolute address within the Memory Data Area where the values will be read. If the region addressed (up to the end of the block) is outside the boundary of the configured data memory then a NAK is returned. The value for Address can be determined from the Data Area of the program Symbol File “<program name>.syf”.

#Bytes: A 2-byte value representing the number of bytes to be read. Byte 1 will be read from Address[0], Byte 2 from Address[1], etc. The minimum number of bytes that can be read is 1 byte. The maximum number of bytes that can be read at one time is 494 bytes. If more than 494 bytes are needed, multiple RDBLKDATA commands can be used.

The Packet Body Length is 6 bytes.

Packet returned by the LSC:

[Header][Byte 1][Byte 2][...][Byte N][Checksum]

Byte1 … N: These represent the byte values for the data to be read. Data bytes read from the LSC are ordered big endian, where the highest order byte is first.

The Packet Body Length is ‘N’ bytes. The maximum Packet Body Length is 494 bytes.
The WRBLKDATA Command (Operation Code 02)

The WRBLKDATA serial command transmits a group of data bytes to the LSC. Each data byte is stored sequentially in the LSC Data Area beginning at the address specified in the WRBLKDATA command.

Packet sent to the LSC:

[Header][Address][getBytes][Byte 1][Byte 2][...][Byte N][Checksum]

Address: A 4-byte value indicating the absolute address within the Memory Data Area where the values will be written. If the region addressed (up to the end of the block) is outside the boundary of the configured data memory then a NAK is returned. The value for Address can be determined from the Data Area of the program Symbol File "<program name>.syf".

getBytes: A 2-byte value representing the number of bytes to be written. Byte 1 will be stored at Address[0], Byte 2 at Address[1], etc. The minimum number of bytes that can be written is 1 byte. The maximum number of bytes that can be written at one time is 490 bytes. If more than 490 bytes are needed, multiple WRBLKDATA commands can be used.

NOTE: The WRBLKDATA commands cannot write to the Constants Area.

Byte1 ... N: These represent the bytes of data to be written. The maximum number of bytes is 490. Data bytes written to the LSC should be ordered big endian, where the highest order byte is first.

The Packet Body Length is 6 + ‘N’ bytes. The maximum Packet Body Length is 496 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The RQSTAT Command (Operation Code 03)

The RQSTAT serial command requests the LSC Controller to transmit its status bits as represented in the table below.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

[Header][Controller Status][Reserved 1][Checksum]

Controller Status: A 4-byte value representing the Controller Status bits. The Controller Status bits definition is shown below.

Reserved: A 10-byte area not currently used.

The Packet Body Length is 14 bytes.

Controller Status Bits

<table>
<thead>
<tr>
<th>Bit</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>DEVICE 1 UP</td>
</tr>
<tr>
<td>1</td>
<td>DEVICE 2 UP</td>
</tr>
<tr>
<td>2</td>
<td>DEVICE 3 UP</td>
</tr>
<tr>
<td>3</td>
<td>DEVICE 4 UP</td>
</tr>
<tr>
<td>4</td>
<td>PROGRAM RUNNING</td>
</tr>
<tr>
<td>5</td>
<td>SYSTEM RESET</td>
</tr>
<tr>
<td>6</td>
<td>AUTO START ENABLED</td>
</tr>
<tr>
<td>7</td>
<td>BAD PROGRAM ARGUMENT</td>
</tr>
<tr>
<td>8</td>
<td>BAD PROGRAM ADDRESS</td>
</tr>
<tr>
<td>9</td>
<td>BAD FLASH MEMORY</td>
</tr>
<tr>
<td>10</td>
<td>SYSTEM TRAP ARMED</td>
</tr>
<tr>
<td>11</td>
<td>LOADING PROGRAM</td>
</tr>
<tr>
<td>12</td>
<td>LOADING PROGRAM ERROR</td>
</tr>
<tr>
<td>13</td>
<td>BAD OPCODE</td>
</tr>
<tr>
<td>14</td>
<td>STACK OVERFLOW</td>
</tr>
<tr>
<td>15</td>
<td>STACK UNDERFLOW</td>
</tr>
<tr>
<td>16</td>
<td>SYSTEM READY</td>
</tr>
<tr>
<td>17</td>
<td>CALCULATING</td>
</tr>
<tr>
<td>18</td>
<td>ON ERROR ENABLED</td>
</tr>
<tr>
<td>19</td>
<td>EVENTS ENABLED</td>
</tr>
<tr>
<td>20</td>
<td>Spare</td>
</tr>
<tr>
<td>21</td>
<td>Spare</td>
</tr>
<tr>
<td>22</td>
<td>Spare</td>
</tr>
<tr>
<td>23</td>
<td>HIGH TEMP WARNING.</td>
</tr>
<tr>
<td>24</td>
<td>Spare</td>
</tr>
<tr>
<td>25</td>
<td>LCBB CFG REQUIRED</td>
</tr>
<tr>
<td>26</td>
<td>LCBB CFG COMPLETE</td>
</tr>
<tr>
<td>27</td>
<td>Spare</td>
</tr>
<tr>
<td>28</td>
<td>Spare</td>
</tr>
<tr>
<td>29</td>
<td>DEVICES READY TO CONFIGURE</td>
</tr>
<tr>
<td>30</td>
<td>ALL DEVICES CONFIGURED</td>
</tr>
<tr>
<td>31</td>
<td>Spare</td>
</tr>
</tbody>
</table>
The SETFLAG Command (Operation Code 04)

This operation code has been preceded by SET_FLAGA (operation code 27).

The SETFLAG serial command can be used to turn on a specified input, output or to set a specified user flag.

Packet sent to the LSC:

**[Header][Flag Type][Flag Number][Checksum]**

**Flag Type:** A 2-byte value indicating the *Flag Type* to be set.

**Flag Number:** A 2-byte value indicating the logical *Flag Number* to be set.

The *Packet Body Length* is 4 bytes.

Packet returned by the LSC:

***None, ACK is only response***

Flag Types

<table>
<thead>
<tr>
<th>Description</th>
<th># Bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Luminary Controller Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Inputs</td>
<td>1</td>
</tr>
<tr>
<td>Device Outputs</td>
<td>1</td>
</tr>
<tr>
<td>Timers</td>
<td>3</td>
</tr>
<tr>
<td>User Flags</td>
<td>32</td>
</tr>
<tr>
<td>Global S/W PLS</td>
<td>8</td>
</tr>
<tr>
<td>Luminary Controller Inputs</td>
<td>2</td>
</tr>
<tr>
<td>Luminary Controller Outputs</td>
<td>2</td>
</tr>
</tbody>
</table>
The CLRFLAG Command (Operation Code 05)

This operation code has been preceded by CLR_FLAGA (operation code 28).

The CLRFLAG serial command can be used to turn off a specified input, output or to clear a specified user flag.

Packet sent to the LSC:

[Header][Flag Type][Flag Number][Checksum]

  Flag Type: A 2-byte value indicating the Flag Type to be set.
  Flag Number: A 2-byte value indicating the logical Flag Number to be set.

The Packet Body Length is 4 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The RDFLAG Command (Operation Code 06)

This operation code has been preceded by RD_FLAGA (operation code 29).

The RDFLAG serial command can be used to read the state of the controller status flags, device status flags, inputs, outputs, timers, user flags, global S/W PLS, global H/W PLS, and local H/W PLS flags.

Packet sent to the LSC:

[Header][Flag Type][Flag Number][Checksum]

Flag Type: A 2-byte value indicating the Flag Type to be set.
Flag Number: A 2-byte value indicating the logical Flag Number to be set.

The Packet Body Length is 4 bytes.

Packet returned by the LSC:

[Header][Flag State][Checksum]

Flag State: A 2-byte value indicating the state of the Flag Number read. If the flag is SET/ON, Flag State will be 1. If the flag is CLEAR/OFF, Flag State will be 0.

The Packet Body Length is 2 bytes.
The RDFLAGGROUP Command (Operation Code 07)

This operation code has been preceded by RD_FLAGA_GROUP (operation code 30).

The RDFLAGGROUP serial command can be used to read the status of all LSC status flags, timers, user flags, global S/W PLS with a single instruction.

Packet sent to the LSC:

[Header][Flag Type][Checksum]

Flag Type: A 2-byte value indicating the Flag Type to be read.

The Packet Body Length is 2 bytes.

Packet returned by the LSC:

[Header][Byte 1][Byte 2]...[Byte N][Checksum]

Byte 1 ... N: These are 8 bit unsigned values representing the state of the requested Flag Types. The flag states are returned as follows:

<table>
<thead>
<tr>
<th>BYTE</th>
<th>BIT #</th>
<th>LOGICAL FLAG #</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>7-0</td>
<td>7-0</td>
</tr>
<tr>
<td>2</td>
<td>7-0</td>
<td>15-8</td>
</tr>
<tr>
<td>3</td>
<td>7-0</td>
<td>23-16</td>
</tr>
<tr>
<td>4</td>
<td>7-0</td>
<td>31-24</td>
</tr>
<tr>
<td>.</td>
<td>.</td>
<td>.</td>
</tr>
<tr>
<td>.</td>
<td>.</td>
<td>.</td>
</tr>
<tr>
<td>.</td>
<td>.</td>
<td>.</td>
</tr>
<tr>
<td>N</td>
<td>7-0</td>
<td>( (N<em>8) – 1 ) – ( (N</em>8) - 8 )</td>
</tr>
</tbody>
</table>

The Packet Body Length is the length in bytes of the Flag Type requested.

Flag Types

<table>
<thead>
<tr>
<th>Description</th>
<th># Bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Luminary Controller Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Inputs</td>
<td>1</td>
</tr>
<tr>
<td>Device Outputs</td>
<td>1</td>
</tr>
<tr>
<td>Timers</td>
<td>3</td>
</tr>
<tr>
<td>User Flags</td>
<td>32</td>
</tr>
<tr>
<td>Global S/W PLS</td>
<td>8</td>
</tr>
<tr>
<td>Luminary Controller Inputs</td>
<td>2</td>
</tr>
<tr>
<td>Luminary Controller Outputs</td>
<td>2</td>
</tr>
</tbody>
</table>
The STOPPRG Command (Operation Code 15)

The STOPPRG serial command is used to stop the execution of the program currently running in the controller.

**Packet sent to the LSC:**

[Header][Checksum]

The Packet Body Length is 0.

**Packet returned by the LSC:**

*** None, ACK is only response ***
The RESETPRG Command (Operation Code 16)

The RESETPRG serial command will erase the program currently stored in the controller memory. The program must be stopped before issuing the RESETPRG command.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

*** None, ACK is only response ***
The STARTPRG Command (Operation Code 17)

The STARTPRG serial command will cause the program currently loaded in the controller to begin executing.

**Packet sent to the LSC:**

[Header][Checksum]

The *Packet Body Length* is 0.

**Packet returned by the LSC:**

*** None, ACK is only response ***
The SETAUTOSTART Command (Operation Code 18)

The SETAUTOSTART serial command will set a status bit in the controller so that every time the power is cycled on the controller the program will automatically begin execution.

*Packet sent to the LSC:*

```
/Header/[Checksum]
```

The *Packet Body Length* is 0 byte.

*Packet returned by the LSC:*

*** None, ACK is only response ***
The CLRAUTOSTART Command (Operation Code 19)

The CLRAUTOSTART serial command will clear a status bit in the controller so that every time the power is cycled on the controller the program will not begin execution.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

*** None, ACK is only response ***
The RDPRGINFO Command (Operation Code 20)

The RDPRGINFO serial command is used to retrieve the details of the Program Area, Data Areas and the System Configuration.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

[Header][Program Info Structure][Reserved][Checksum]

Program Info Structure: A 240 byte String containing detailed information about the program.

<table>
<thead>
<tr>
<th>Field</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>Filename</td>
<td>32 Byte String</td>
</tr>
<tr>
<td>Date</td>
<td>12 Byte String</td>
</tr>
<tr>
<td>Time</td>
<td>12 Byte String</td>
</tr>
<tr>
<td>Version</td>
<td>16 Byte String</td>
</tr>
<tr>
<td>Reserved</td>
<td>24 Byte String</td>
</tr>
<tr>
<td>Memory Org - Program Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Memory Org - Constants Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Memory Org - Variables Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Memory Org - Extended Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Memory Org - Cfg Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Memory Org - Fixed Memory Area</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Reserved</td>
<td>24 Bytes</td>
</tr>
<tr>
<td>Extended Area Size - SHORTS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Extended Area Size - LONGS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Extended Area Size - FLOATS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Actual Program Area Size</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Program Area Size</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Equate Area Size - SHORTS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Equate Area Size - LONGS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Equate Area Size - FLOATS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Constant Area Size - SHORTS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Constant Area Size - LONGS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Constant Area Size - FLOATS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Variable Area Size - SHORTS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Variable Area Size - LONGS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Variable Area Size - FLOATS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Variable Area Size - TEXT</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Cfg Area Size - Devices</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Reserved</td>
<td>8 Bytes</td>
</tr>
<tr>
<td>Fixed Memory Area Size - SHORTS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Fixed Memory Area Size - LONGS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Fixed Memory Area Size - FLOATS</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Reserved</td>
<td>8 Byte String</td>
</tr>
<tr>
<td>Program Check Sum</td>
<td>Long Integer</td>
</tr>
<tr>
<td>Reserved</td>
<td>240 Bytes</td>
</tr>
</tbody>
</table>

The Packet Body Length is 480 bytes.
The RDCNTRLINFO Command (Operation Code 21)

The RDCNTRLINFO serial command is used to retrieve the details of the Controllers present hardware and software configuration.

*Packet sent to the LSC:*

```
/Header/[Checksum]
```

The *Packet Body Length* is 0.

*Packet returned by the LSC:*

```
/Header/[Controller Info Structure]/[Checksum]
```

*Controller Info Structure:* Data structure of 248 bytes.

- **Firmware Number/Rev String** - 12 Byte String
- **Spare** - 140 Bytes
- **Powerup Count (from NVRAM)** - 4 Byte Value
- **Spare** - 52 Bytes
- **Last Error Exception**
  - **Controller Status Register** - 4 Byte Value
  - **Power Count** - 4 Byte Value
  - **MacroProgram Instruction Ptr** - 4 Byte Value
- **Spare** - 28 Bytes

The *Packet Body Length* is 248 bytes.
The RDWATCHDATA Command (Operation Code 26)

The RDWATCHDATA serial command reads a group of data bytes from the LSC. Each data value will be read from the LSC based on the type and the Address/Logical Number.

*Packet sent to the LSC:*

[Header][Type 1]...[Type 16][Address/Logical 1]...[Address/Logical 16][Checksum]

- **Type 1 ... 16:** 16 2-byte values indicating the type to be read. *Type 1 corresponds to Address 1.*
- **Addr 1 ... 16:** 16 4-byte values representing the address or logical number of the desired type. *Address 1 corresponds to Type 1.*

The *Packet Body Length* is 96 bytes.

*Packet returned by the LSC:*

[Header][Value 1]...[Value 16][Checksum]

- **Value 1 ... 16:** 16 8-byte values representing the data or flag state of the corresponding *Type.*

The *Packet Body Length* is 128 bytes.
The SET_FLAGA Command (Operation Code 27)

The SET_FLAGA serial command can be used to set (enable) all flags, includes network addressing capability; i.e. controller Devices.

Packet sent to the LSC:

[Header][Device Number][Flag Type][Physical Flag Number][Checksum]

Device Number: A 2-byte value indicating the logical device number of the device or zero if not needed for the desired Flag Type.

Flag Type: A 2-byte value indicating the Flag Type to be set.

Physical Flag Number: A 2-byte value indicating the Physical Flag Number to be set.

The Packet Body Length is 6 bytes.

Packet returned by the LSC:

Typical response is an ACK. Error response 0x14 will be returned for read only Flag Types.
The CLR_FLAGS Command (Operation Code 28)

The CLR_FLAGS serial command can be used to clear (disable) all flags, includes network addressing capability; i.e. controller Devices.

**Packet sent to the LSC:**

[Header][Device Number][Flag Type][Physical Flag Number][Checksum]

- **Device Number:** A 2-byte value indicating the logical Device Number of the device, zero if not needed for desired Flag Type.
- **Flag Type:** A 2-byte value indicating the Flag Type to be cleared.
- **Physical Flag Number:** A 2-byte value indicating the Physical Flag Number to be cleared.

The Packet Body Length is 6 bytes.

**Packet returned by the LSC:**

Typical response is an ACK. Error response 0x14 will be returned for read only Flag Types.
The RD_FLAGA Command (Operation Code 29)

The RD_FLAGA serial command can be used to read all flags, includes network addressing capability; i.e. controller Devices.

Packet sent to the LSC:

[Header][Device Number][Flag Type][Physical Flag Number][Checksum]

Device Number: A 2-byte value indicating the logical Device Number of the device, zero if not needed for desired Flag Type.

Flag Type: A 2-byte value indicating the Flag Type to be read.

Physical Flag Number: A 2-byte value indicating the Physical Flag Number to be read.

The Packet Body Length is 6 bytes.

Packet returned by the LSC:

[Header][Device Number][Flag Type][Physical Flag Number][Flag State][Checksum]

Device Number: A 2-byte value indicating the logical Device Number of the device, zero if not needed for desired Flag Type.

Flag Type: A 2-byte value indicating the Flag Type read.

Physical Flag Number: A 2-byte value indicating the Physical Flag Number read.

Flag State: A 2-byte value indicating the state of the Flag Number read. If the flag is SET/ON, Flag State will be 1. If the flag is CLEAR/OFF, Flag State will be 0.

The Packet Body Length is 8 bytes.
The RD_FLAGA_GROUP Command (Operation Code 30)

The RD_FLAGA_GROUP serial command can be used to read all flags, includes network addressing capability; i.e. controller Devices.

*Packet sent to the LSC:*

```
[Header][Device Number][Flag Type][Number of Bytes][Checksum]
```

**Device Number:** A 2-byte value indicating the logical *Device Number* of the device whose status flags are to be read, zero if not needed by desired flag type.

**Flag Type:** A 2-byte value indicating the type of device flag to be read.

All Flag types VALID, however *Device Number* is only used for flag types 2, 3, and 4. For other flag types *Device Number* should be zero.

**Number of Bytes:** A 2-byte value indicating the *Number of Bytes* to read. This value is typically of a range from 1-16 bytes. The controller's maximum packet length of 512 total bytes should not be exceeded, if so the controller will respond with a error NAK value of 0x55. Requesting more than the available number of bytes in any *Flag Type* will return data from the adjacent flag type organized in the controller's memory.

The *Packet Body Length* is 6 bytes.

*Packet returned by the LSC:*

```
[Header][Device Number][Flag Type][Number of Bytes][Byte 1][Byte 2]...[Byte N][Checksum]
```

**Device Number:** A 2-byte value indicating the logical *Device Number* of the device whose status flags are to be read.

**Flag Type:** A 2-byte value indicating the type of device flag to be read.

All flag types VALID, however device number is only used for flag types 2, 3, and 4. For other flag types, *Device Number* should be zero.

**Number of Bytes:** A 2-byte value indicating the *Number of Bytes* to read. This value is typically of a range from 1-16 bytes. The controller's maximum packet length of 512 total bytes should not be exceeded, if so the controller will respond with a error NAK value of 0x55. Requesting more than the available number of bytes in any *Flag Type* will return data from the adjacent flag type organized in the controller's memory.
Byte 1 ... N: These are 8-bit unsigned values representing the state of the requested flags. The flag states are returned as follows:

- **Byte 1** Bit 0 is flag 1...Bit 7 is flag 8
- **Byte 2** Bit 0 is flag 9...Bit 7 is flag 16
- **Byte 3** Bit 0 is flag 17...Bit 7 is flag 24

- **Byte N** Bit 0 is flag (((N-1) x 8) +1)...Bit 7 is flag (N x 8)

The *Packet Body Length* is 6 + ‘N’ bytes.

### Flag Types

<table>
<thead>
<tr>
<th>Description</th>
<th># Bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Luminary Controller Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Status</td>
<td>4</td>
</tr>
<tr>
<td>Device Inputs</td>
<td>1</td>
</tr>
<tr>
<td>Device Outputs</td>
<td>1</td>
</tr>
<tr>
<td>Timers</td>
<td>3</td>
</tr>
<tr>
<td>User Flags</td>
<td>32</td>
</tr>
<tr>
<td>Global S/W PLS</td>
<td>8</td>
</tr>
<tr>
<td>Luminary Controller Inputs</td>
<td>2</td>
</tr>
<tr>
<td>Luminary Controller Outputs</td>
<td>2</td>
</tr>
</tbody>
</table>
The RDEVENTINFO Command (Operation Code 31)

The RDEVENTINFO serial command is used to retrieve the status of programmed Events. The status indicates the events that are enabled and the status of the event scan mode.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

[Header][Scan Mode][Event State 64 to 33][Event State 32 to 1][Reserved][Checksum]

Scan Mode: A 2-byte value indicating the Scan Mode. A 1 indicates the scan mode is on and 0 indicates the scan mode is off.

Event State 64 to 33: A 4-byte value indicating the state of events 64 to 33.

A 1 in the appropriate bit position indicates that the event is enabled and a 0 indicates that the corresponding event is disabled.

Event State 32 to 1: A 4-byte value indicating the state of events 32 to 1.

A 1 in the appropriate bit position indicates that the event is enabled and a 0 indicates that the corresponding event is disabled.

Reserved: A 8-byte value currently not used.

The Packet Body Length is 18 bytes.
The RDERRORLOG Command (Operation Code 35)

The RDERRORLOG serial command is used to retrieve the information stored for the last 8 program error conditions.

Packet sent to the LSC:

[Header][Checksum]

The Packet Body Length is 0.

Packet returned by the LSC:

[Header][Error 1 Info] ... [Error 8 Info][Checksum]

Error 'N' Info: 2-byte value indicating the enabled status for that error
2-byte value indicating the error number
4-byte value indicating the address of the error routine to be executed
4-byte value indicating the address of the program command in error
2-byte value indicating the address of the program line in error
2-byte value indicating the index to be used with the power up counter
4-byte value indicating the power up counter
4-byte spare
4-byte spare

The returned Packet Body Length is 224 bytes.
The CLRFIXEDMEM Command (Operation Code 36)

The CLRFIXEDMEM serial command will erase the Fixed Variable Memory area stored in the controller NOV memory. The program must be stopped before issuing the CLRFIXEDMEM command.

*Packet sent to the LSC:*

```
[Header][Checksum]
```

The *Packet Body Length* is 0.

*Packet returned by the LSC:*

*** None, ACK is only response ***
The WRPRG Command (Operation Code 64)

The WRPRG serial command will initialize the transmission of the application program and transmit the first block of program or program data to the LSC Controller. Each data byte is stored sequentially in the LSC Data Area beginning at the address specified in the WRPRG command.

This command can only be used for the first application program packet. Additional program packets should be transmitted with the WRMOREPRG Command (Operation Code 65). The end of program transmission is signified by the transmission of the WRPRGINFO (Operation Code 66).

Packet sent to the LSC:

[Header][Address][#Bytes][Byte 1][Byte 2]…[Byte N][Checksum]

Address: A 4-byte value indicating the absolute Address within the Memory Data Area where the values will be written. If the region addressed (up to the end of the block) is outside the boundaries of the configured data memory then a NAK is returned. The value for Address can be determined from the Data Area of the program Symbol File "<program name>.syf".

#Bytes: A 2-byte value representing the number of bytes to be written. Byte 1 will be stored at Address[0], Byte 2 at Address[1], etc. The minimum number of bytes that can be written is 1 byte. The maximum number of bytes that can be written at one time is 495 bytes. If more than 490 bytes are needed to transmit the program, additional WRMOREPRG commands can be used.

Byte 1 … N: These represent the bytes of data to be written.

The Packet Body Length is 6 + ‘N’ bytes. The maximum Packet Body Length is 496 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The WRMOREPRG Command (Operation Code 65)

The WRMOREPRG serial command serves to transmit additional blocks of program or program data to the LSC Controller. Each data byte is stored sequentially in the LSC Data Area beginning at the address specified in the WRMOREPRG command.

This command cannot be used for the first program packet. The end of program transmission is signified by the transmission of the WRPRGINFO (Operation Code 66).

Packet sent to the LSC:

\[
\text{[Header][Address][#Bytes][Byte 1][Byte 2]...[Byte N][Checksum]}\]

- **#Bytes**: A 2-byte value representing the number of bytes to be written. Byte 1 will be stored at Address[0], Byte 2 at Address[1], etc. The minimum number of bytes that can be written is 1 byte. The maximum number of bytes that can be written at one time is 490 bytes. If more than 490 bytes are needed to transmit the program, additional WRITEMOREMACRO commands can be used.

- **Address**: A 4-byte value indicating the absolute address within the Memory Data Area where the values will be written. If the region addressed (up to the end of the block) is outside the boundaries of the configured data memory then a NAK is returned. The value for Address can be determined from the Data Area of the program Symbol File “<program name>.syf”.

- **Byte 1 ... N**: These represent the bytes of data to be written.

The Packet Body Length is 6 + ‘N’ bytes. The maximum Packet Body Length is 496 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The WRPRGINFO Command (Operation Code 66)

The WRPRGINFO serial command is used to define the details of the Program Area, Data Areas and the System Configuration. It also is used to signify to the controller that this is the last packet to be downloaded for the program.

Packet sent to the LSC:

[Header][Program Info Structure][Reserved][Checksum]

Program Info Structure: A 240 byte String containing detailed information about the program.

Filename - 32 Byte String
Date - 12 Byte String
Time - 12 Byte String
Version - 16 Byte String
Reserved - 24 Byte String
Memory Org - Program Area - Long Integer
Memory Org - Constants Area - Long Integer
Memory Org - Variables Area - Long Integer
Memory Org - Extended Area - Long Integer
Memory Org - Cfg Area - Long Integer
Memory Org - Fixed Memory Area - Long Integer
Reserved - 24 Bytes
Extended Area Size - SHORTS - Long Integer
Extended Area Size - LONGS - Long Integer
Extended Area Size - FLOATS - Long Integer
Actual Program Area Size - Long Integer
Program Area Size - Long Integer
Equate Area Size - SHORTS - Long Integer
Equate Area Size - LONGS - Long Integer
Equate Area Size - FLOATS - Long Integer
Constant Area Size - SHORTS - Long Integer
Constant Area Size - LONGS - Long Integer
Constant Area Size - FLOATS - Long Integer
Variable Area Size - SHORTS - Long Integer
Variable Area Size - LONGS - Long Integer
Variable Area Size - FLOATS - Long Integer
Variable Area Size - TEXT - Long Integer
Cfg Area Size - Devices - Long Integer
Reserved - 8 Bytes
Fixed Memory Area Size - SHORTS - Long Integer
Fixed Memory Area Size - LONGS - Long Integer
Fixed Memory Area Size - FLOATS - Long Integer
Reserved - 8 Byte String
Program Check Sum - Long Integer
Reserved - 240 Bytes

The Packet Body Length is 480 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The RQTRCSTAT Command (Operation Code 68)

The RQTRCSTAT serial command returns the LSC controller trace status as a 2-byte value.

Packet sent to the LSC:

[Header][Reserved][Checksum]

Reserved: A 2-byte value currently not used. Default is 0x0000.

The Packet Body Length is 2 bytes.

Packet returned by the LSC:

[Header][Status][Reserved][Checksum]

Status: A 2-byte value defined as follows:

(0) The controller has stopped tracing or was never requested to start tracing.
(1) The trace is in progress but the Trace Enable line has not yet been detected.
(>1) The trace is in progress, the Trace Enable line has been detected but the Trace Trigger line has not been detected.

Reserved: A 2-byte value currently not used. Default is 0x0000.

The Packet Body Length is 4 bytes.
The STARTTRC Command (Operation Code 69)

The STARTTRC serial command requests the LSC controller to begin tracing program execution as specified in a previous WRTRCSETUP serial command. Program execution (source line numbers) will be placed in the trace buffer until the WRTRCSETUP conditions are met or until a STOPTRC command is received.

Packet sent to the LSC:

[Header][Reserved][Checksum]

Reserved: A 2-byte value currently not used. Default is 0x0000.

The Packet Body Length is 2 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The STOPTRC Command (Operation Code 70)

The STOPTRC serial command requests the LSC controller to disable the trace feature. Retrieving trace information from the trace buffer may not be reliable, as a result of issuing a STOPTRC command.

Packet sent to the LSC:

```
[Header][Reserved][Checksum]
```

Reserved: A 2-byte value currently not used. Default is 0x0000.

The Packet Body Length is 2 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The WRTRCSETUP Command (Operation Code 71)

The WRTRCSETUP serial command requests the LSC controller to perform a real time trace of the instructions being executed based on the list of parameters below.

Packet sent to the LSC:

[Header][Mode][Trace Enable][Trace Trigger][Lines to Trace][Var 1 Address]
[Var 2 Address][Var 1 Type][Var 2 Type][Reserved][Checksum]

Mode: A 2-byte value defined as follows:
(2) Trace Before - store the line numbers of the instructions executed just before the Trace Trigger.
(3) Trace About - store the line numbers of the instructions executed just before and just after the Trace Trigger.
(4) Trace After - store the line numbers of the instructions executed just after the Trace Trigger.

Trace Enable: A 2-byte value indicating the line number which, when encountered, will cause the LSC controller to begin loading its' trace buffer and monitoring the Trace Trigger.

Trace Trigger: A 2-byte value indicating the line number which keys the actual trace buffer display based on before, after or about.

Lines to Trace: A 2-byte value indicating the number of lines to be traced. This value is based on the Var 1 Type and Var 2 Type entries:
- 240 - if there is no request to monitor Var 1 or Var 2
- 60 - if the largest Var Type to monitor is type FLOAT
- 120 - if the largest Var Type to monitor is type LONG
- 240 - if the largest Var Type to monitor is type SHORT

Var 1 Address: A 4-byte value indicating the address of a variable to be monitored at each trapped trace line.

Var 2 Address: A 4-byte value indicating the address of a variable to be monitored at each trapped trace line.

Var 1 Type: A 1-byte value indicating the type of data (SHORT = 7, LONG = 8, FLOAT = 9) to be monitored at each trapped trace line.

Var 2 Type: A 1-byte value indicating the type of data (SHORT = 7, LONG = 8, FLOAT = 9) to be monitored at each trapped trace line.

Reserved: A 2-byte value currently not used. Default is 0x0000.

The Packet Body Length is 20 bytes.

Packet returned by the LSC:

*** None, ACK is only response ***
The RDTRCLINES Command (Operation Code 72)

During a trace operation, the LSC Controller stores the line number of each instruction executed into a trace buffer. Each line number corresponds to a line from the program Source File "<program name>.srf". Although the RDTRCLINES command returns a fixed length packet and buffer size, the number of lines in the buffer is dependent on whether a request was made to monitor variables as well as the size of the variables.

**Note:** The number of lines traced in the buffer can vary between 60, 120 and 240 based on the trace option to monitor variables during trace execution. Depending on the size of the largest data variable to be monitored, the number of traced program lines / variables can be reduced to as few as 60.

**Packet sent to the LSC:**

```
[Header][Reserved][Checksum]
```

**Reserved:** A 2-byte value currently not used. Default is 0x0000.

The **Packet Body Length** is 2 bytes.

**Packet returned by the LSC:**

```
[Header][Mode][Enable Line][Trigger Line][Mark][Trace Line 1]...[Trace Line N][Checksum]
```

**Mode:** A 2-byte value defined as follows:

- (2) Trace Before - store the line numbers of the instructions executed just **before** the Trace Trigger.
- (3) Trace About - store the line numbers of the instructions executed just **before** and just **after** the Trace Trigger.
- (4) Trace After - store the line numbers of the instructions executed just **after** the Trace Trigger.

**Enable Line:** A 2-byte value indicating the line number which, when encountered, will cause the LSC controller to begin loading its’ trace buffer and monitoring the Trace Trigger.

**Trigger Line:** A 2-byte value indicating the line number which keys the actual trace buffer display based on before, after or about.

**Mark:** A 2-byte value acting as a pointer into the trace buffer indicating the location of the Trigger Line.

**Lines Traced:** A 2-byte value indicating the actual number of lines in trace buffer. This value should match the number of Lines to Trace as indicated in the WRTRCSETUP command.

**Trace Line 1 … N:** This is a 480-byte area which will hold from 60 to 240 traced lines, depending on the requested number of Lines to Trace as indicated in the WRTRCSETUP command.

The **Packet Body Length** is 490 bytes.
The RDTRCVAR (1, 2) Command  (Operation Codes 73, 74)

During a trace operation, the LSC Controller will store the value of up to two variables (Variable 1 and/or Variable 2 as defined in the WRTRCSETUP command) as each line is executed and stored in the trace buffer. This gives the user an opportunity to “monitor” the contents of two variables (SHORT, LONG or FLOAT) during program execution.

**Note:** This is a Trace option and is not mandatory. Up to 240 program lines / variables can be traced if this option is not invoked. Depending on the size of the largest data variable to be monitored, the number of traced program lines / variables can be reduced to as few as 60.

**Packet sent to the LSC:**

```
[Header][Reserved][Checksum]
```

Reserved: A 2-byte value currently not used. Default is 0x0000.

The **Packet Body Length** is 2 bytes. The **Total Packet Length** is 14 bytes.

**Packet returned by the LSC:**

```
[Header][Var Addr][Reserved][Var Type][Data Count][Value 1] … [Value N][Checksum]
```

Var Addr: A 4-byte value indicating the address of the variable monitored at each trapped trace line.

Reserved: A 1-byte reserved value.

Var Type: A 1-byte value indicating the type of data (SHORT, LONG or FLOAT) monitored at each trapped trace line.

Data Count: A 2-byte value indicating the actual number of data values traced and thus the number of entries in the variable trace buffer. This value should match the number of Lines to Trace as indicated in the WRTRCSETUP command.

Value 1 … N: This is a 480-byte area which will hold from 60 to 240 trapped values, depending on the requested number of Lines to Trace as indicated in the WRTRCSETUP command.

The **Packet Body Length** is 488 bytes.